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PART B - FEE(S) TRANSMITTAL

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR			ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/757,404	01/08/2001	Steven A. Gu		Guccione	X-786 US	6382
TITLE OF INVENTION; METHOD AND SYSTEM FOR DEVICE-LE			L SIMULA	TION OF A CIRCUIT DESIGN	n for a programmabli	E LOGIC DEVICE
THE OF MY PROPERTY.			-			
APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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XILINX, INC.		S	San Jose, California 95124			
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This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450						
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P. O. Box 1450

Alexandria, Virginia

22313-1450 Inventor(s):

Steven A. Guccione

Assignee: XILINX, INC.

Serial No.: 09/757,404

conf.no. 6382

Filed:

January 8, 2001

Title: Method and System for Device-Level Simulation of a Circuit Design for a

Programmable Logic Device

Docket No.:

X-786 US

Enclosed: Return Receipt Postcard

Fee Transmittal Form - Part B

Date: April 21, 2005

VIA FACSIMILE 703-746-4000

Atty/Sec: KAC/pbs

